

Using Copper PCB Etch for Low Value Resistance

by Larry Spaziani

INTRODUCTION

Current sensing in power supplies and motor controls demand the use of a very low value resistor. Each application varies in need for resistance value, power rating, size, form factor, inductance, temperature coefficient and accuracy. To meet some of these applications, the copper of a printed circuit board (PCB) can be utilized, but has some distinct limitations. This design note provides design equations and recommendations for designing resistors with PCB copper.

DESIGN OF A COPPER PCB RESISTOR

The resistance, as a function of temperature, for a piece of metal, is given by the equation:

$$R(T) = \frac{S(T) \cdot l}{a}$$

with the units

- R = Resistance, Ω
- S(T) = Resistivity, $\Omega\text{-cm}$
- l = Length, cm
- a = Area, cm^2

The characteristics of copper which are pertinent to the design of a copper PCB resistor are [1]

Electrical Resistivity:

$$1.7241 \cdot 10^{-6} \Omega\text{-cm} @ 20^\circ\text{C}$$

Temperature Coefficient of Resistivity:

$$+0.0039 \text{ per } ^\circ\text{C}$$

Composition:

99.5% Pure Copper, typically

These constants apply to the standard commercially annealed copper used in PCB technology.

The resistivity of copper, as a function of temperature, is therefore defined as:

$$S(T) = 1.7241 \cdot 10^{-6} \cdot [1 + 0.0039 \cdot (T - 20)] \Omega\text{-cm}$$

where T is the copper temperature in $^\circ\text{C}$.

One PCB Definition which is also pertinent to resistor design is:

1oz copper is defined as 1 ounce of copper deposited over 1 square foot of surface area. This results in a copper clad PCB with a typical copper thickness of 0.0014 inches ± 0.0002 inches. 2oz copper is simply twice as thick.

Using these parameters to calculate the resistance of a given length, width, and thickness of copper PCB etch results in the formula:

$$R(T) = \frac{S(T)[\Omega\text{-cm}] \cdot \text{Length}[\text{cm}]}{\text{Width}[\text{cm}] \cdot \text{Thickness}[\text{cm}]}$$

$$= \frac{S(T)[\Omega\text{-cm}] \cdot \frac{1000\text{mils}}{2.54\text{cm}} \cdot \text{Length}(\text{mils})}{\text{Width}(\text{mils}) \cdot \text{Thickness}(\text{mils})}$$

The use of a low value sense resistor implies that the current in that resistor can be quite high. A copper etch on a PCB will self heat due to the power dissipated by the resistor. MIL-STD-275E [2] provides design guidelines relating copper etch current to temperature rise and etch dimensions. Figure 1 recreates sections of the MIL-STD-275E curves. A temperature rise, above ambient temperature, can be found by knowing the current and the area of the copper etch.

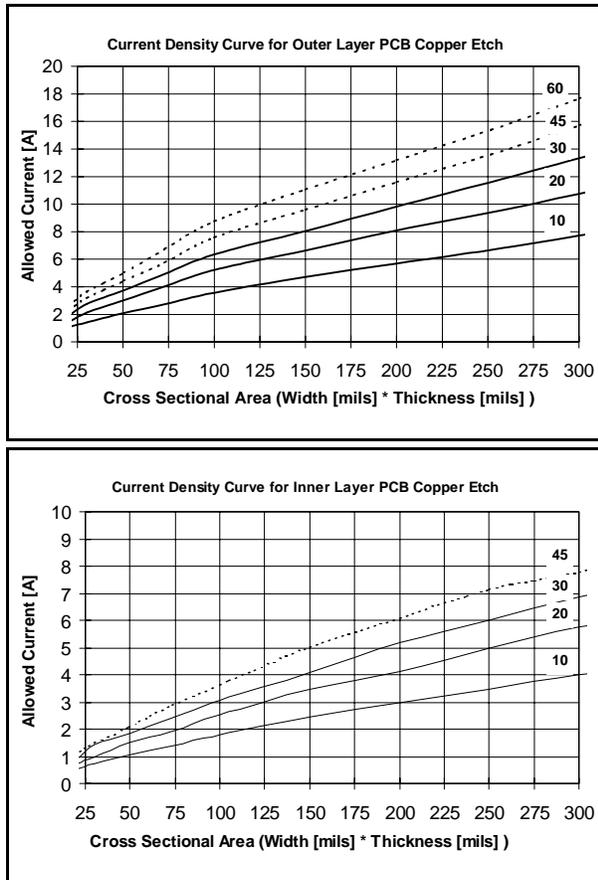


Figure 1. MIL-STD-275E Current Density vs Temperature Rise Curve for Outer and Inner Copper Layers

Example: Calculate the length and width of a 10mΩ MAXIMUM PCB resistor using 1oz on an outer layer of a PCB. The resistor must carry 10A maximum while maintaining no more than a 30°C temperature rise above ambient. Ambient temperature for normal operation is 10°C to 60°C.

Step 1:

Find the cross sectional area to carry 10A with ≤ 30°C rise, and solve for the minimum width of the resistor.

From Figure 1, 205 mils² are required to carry 10 Amperes. 1oz copper is 1.4mils thick, resulting in a minimum width for the resistor of 146mils.

Set Width = 150mils

Step 2:

Find the length of the resistor, insuring that it is designed for 10mΩ maximum at 60°C ambient +30°C rise (copper temperature is 90°C).

From the equations

$$S(90) = 1.7241 \cdot 10^{-6} \cdot [1 + 0.0039 \cdot (90 - 20)] \Omega - \text{cm}$$

and

$$R(90) = \frac{S(90) \left[\frac{\Omega - \text{cm}}{^\circ\text{C}} \right] \cdot \frac{1000\text{mils}}{2.54\text{cm}} \cdot \text{Length}[\text{mils}]}{\text{Width}[\text{mils}] \cdot \text{Thickness}[\text{mils}]}$$

Solving for R(90°C) = 10mΩ, using 150 mils for Width and 1.4 mils for Thickness results in Length = 2.43 inches.

Final Dimensions (inches): 2.43 (L) x 0.150 (W) x 0.0014 (T)

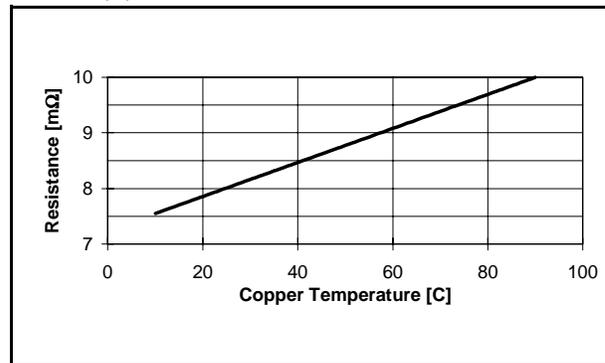


Figure 2. Resistance vs Copper Temperature for Example Design

The final resistance, as a function of the copper temperature, is shown in Figure 2.

Table 1 provides the required dimensions for a 1oz PCB copper resistor given a maximum current and desired voltage drop. Table 1 assumes a maximum operating ambient temperature of 60°C, with the width specified for a 30°C temperature rise. The required resistance is equal to V/I and is calculated at a copper temperature of 90°C.

AMPS	Desired Voltage Drop			Width [in]
	10mV	25mV	50mV	
	PCB Etch Length [in]			
1	0.162	0.405	0.810	0.010
2	0.243	0.608	1.215	0.015
3	0.405	1.013	2.025	0.025
4	0.648	1.620	3.240	0.040
5	0.891	2.228	4.456	0.055
6	1.053	2.633	5.266	0.065
7	1.377	3.443	6.886	0.085
8	1.701	4.253	8.506	0.105
9	2.025	5.063	10.126	0.125
10	2.430	6.076	12.152	0.150
11	2.754	6.886	13.772	0.170
12	3.078	7.696	15.392	0.190

Table1. Dimension Solver for Given Current and Desired Voltage Drop for 90°C Maximum Copper Temperature

Some PCB physical and layout characteristics should be considered when designing a PCB resistor.

- The thickness tolerance of PCB copper may vary from supplier to supplier and relative to location on a PCB. A typical tolerance is ± 0.2 mils/oz.
- Etchback will reduce the copper area, thus increasing the resistance per unit length. Etchback has a larger impact with narrow runs, where the width/thickness ratio is lower. It is recommended that widths of ≥ 0.025 inches be used.
- The copper resistor, if it is on an outer PCB layer, should be solder masked over the entire resistive area. Solder on the copper will reduce the resistance.

- Vias through the resistor will effect the resistance.
- Curved or serpentine resistor patterns may be utilized as long as the overall width and length, including curves, is understood.
- A copper resistor connected to two larger copper planes will have a lower temperature rise than predicted by the MIL-STD-275E curves due to the heatsinking of those copper planes.

CONCLUSIONS

The design of a PCB copper resistor is straightforward once operating parameters such as voltage drop, operating current and operating ambient temperature are known. Tolerances due to PCB technology will effect the accuracy of a PCB resistor and should be considered. The length-to-width ratio of a PCB resistor is quite large due to the low resistivity of copper, but when the area is available on a PCB, this resistor is essentially free.

Applications which require current limiting are ideal for use with a PCB resistor, as current limiting can be set quite accurately at the maximum operating temperature where current limiting is most critical. Average current mode control applications require precise voltage drops independent of temperature and current levels. PCB resistors are therefore not recommended for use as an Average Current Mode control sense resistor.

REFERENCES

- [1] Reference Data for Radio Engineers, Howard W. Sams & Co. Inc., Sixth Edition 1977.
- [2] MIL-STD-275E, NOTICE 1, 8 July 1986, Military Standard Printed Wiring for Electronic Equipment.

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Customers are responsible for their applications using TI components.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, license, warranty or endorsement thereof.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations and notices. Representation or reproduction of this information with alteration voids all warranties provided for an associated TI product or service, is an unfair and deceptive business practice, and TI is not responsible nor liable for any such use.

Resale of TI's products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service, is an unfair and deceptive business practice, and TI is not responsible nor liable for any such use.

Also see: [Standard Terms and Conditions of Sale for Semiconductor Products](http://www.ti.com/sc/docs/stdterms.htm). www.ti.com/sc/docs/stdterms.htm

Mailing Address:

Texas Instruments
Post Office Box 655303
Dallas, Texas 75265